



Project Description:

On-site FPGA and ASIC design, development and verification services for the graphics industry's leading hardware innovator.

Key Features / Project Highlights:

- Design and Verification of a 2.5 GHz, full-width PCI Express Switch with one 16-lane upstream port and two 16-lane downstream ports
- Design and Verification of the next generation PCI Express Switch compliant with Specification 2.0
- Partitioning and mapping of multiple ASICs-in-design to FPGA-based development platforms for verification and early software development
- Created a RAM generator program to produce FPGA-optimized versions of ASIC RAM libraries
- Developed replacement SDRAM and DDR interfaces for use in FPGA-based ASIC verification system
- Developed Time Domain Multiplexing interfaces to increase signal density between multiple 1000+ pin FPGA devices in large verification system

About LocoLabs:

LocoLabs is a Silicon Valley, CA provider of custom and turnkey development platforms and modules for embedded designers. It is our mission to accelerate your product development, drawing on more than a decade of experience tackling tough embedded design problems for leading suppliers of consumer, computing and commercial systems.

In our engineering and design work, LocoLabs customers call us the "Rev-Zero" company. With success stories in consumer electronics, commercial robotics, HD video and instantly deployable wireless networking, our experience reinforces the value of fast prototyping and hardware/software co-design. LocoLabs' cross-trained Embedded, Multimedia, Wireless and Linux experts in our Silicon Valley design center are available to support your rapid product development.

